



No. 4/303

Date: 05.07.2022

Recruitment- Junior Research Fellow (JRF)- (01)

The eligible candidates meeting the following requirements and desiring to be considered for the above mentioned post on purely contract basis for research project entitled “*Hardware Implementation of 3SS Inverter Configuration in Solar GridTied Systems for Effective Energy Utilization Using Wide Bandgap Switches*” funded by the Science and engineering research board (SERB), Ministry of Science and Technology, Government of India; at the institute are hereby informed to submit the application with the required details for the online Interview/tests at the date and time (to be declared after the last date of receiving the applications). Candidates having similar previous experience will be preferred. Selected candidate may get the chance to enroll for Ph.D. programme. For any further information, please contact **Dr.JAMMALAVENKATARAMANIAH (PI) through e-mail (ramana@eed.svnit.ac.in, jvenkataramana.ee@gmail.com)** or visit www.svnit.ac.in.

Eligibility & Experience:

- First Class M. E. /M.Tech. in Power Electronics and Drives or Allied branches with B.E./B.Tech. in Electrical Engineering or Electrical and Electronics Engineering.
- Candidate having experience in the field of above mentioned project title will be given preference.
- Candidate having qualified GATE will be given preference

Tenure: Initially 01 Year, extendable based on the performance till the project continues.

Monthly Emoluments:

First & Second Year: Monthly, Rs. 31000.00/- and HRA as per the SERB-JRF rules

Third Year: Monthly, Rs. 35000.00/- and HRA as per the SERB-JRF rules

Important Dates

Last Date to Apply: 24 July 2022, 17:00 hr.

Display of eligible candidates list on Institute Website: 26 July 2022

Tentative date of interview/ test: First week of August 2022(Exact schedule will be displayed on the institute website along with list of eligible candidates)

Link for submission of application form: <https://forms.gle/KhJcPscZ6Nkb3Vyb6>

Interested candidates will be required to note the following:

- i. The interested candidates will be required to submit their application form along with all essential details and scanned copy of all their original certificates, mark sheets and a recent passport size photograph through the above mentioned link within last date, i.e, 24 July 2022, 17:00 hr.
- ii. Candidates need to bring the complete hardcopy set of the application form with xerox copies of all required documents on the day of interview for verification purpose.
- iii. Candidates are requested to positively mention their e-mail ID and Mobile Number in the Application failing which their candidature may be rejected.
- iv. The applications received within the last date & time stipulated will be screened by a selection committee, and a list of eligible candidates shortlisted for interview/test will be displayed on the Institute's official website along with the date and time schedule of the interview/test. The Screening Committee will devise its own criteria for shortlisting of candidates for interview. Merely fulfilling main eligibility criteria may not be suitable for eligible to be invited for the interview/test.
- v. The decision of SVNIT-Surat in all matters relating to eligibility, acceptance, screening, mode of selection will be final and binding on the candidates and no enquiry correspondence will be entertained in this connection from any individual or any agency on behalf of the candidate.
- vi. The original documents of selected candidates will be verified at the time of Interview, and in the event of any information mentioned in application/CV/testimonials found false or incorrect, their candidature straightway shall be cancelled.
- vii. No TA/DA will be paid for attending the interview/test.

PI

HoD,DoEE

Dean R&C

DIRECTOR



Sardar Vallabhbhai National Institute of Technology, Surat - 395007, Gujarat
Department of Electrical Engineering

Application form for the post of JRF

Application for the post of JRF at Department of Electrical Engineering, SVNIT, Surat, Gujarat

Title of project: Hardware Implementation of 3SS Inverter Configuration in Solar GridTied Systems for Effective Energy Utilization Using Wide Bandgap Switches

For office use only:

Serial Number:

Eligible for Interview: YES/NO

Checked the Certificates:

Paste self-attested recent photograph

1. Name:

2. Address:

3. Date of birth:

4. Category (General/SC/ST/OBC):

5. Contact details:

(i) Email ID:

(ii) Mobile No.:

6. Educational qualifications:

Examination Passed	Board/University	Subject	Year	Percentage/ CGPA	Division/ Class
M.E./M.Tech.					
B.Tech.					
12 th /Diploma					
Other					

7. B. Tech project details:

8. M. Tech project details:



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9. Professional experience:

Organization	Post held	From	To	Pay Drawn	Nature of Duties

10. Publications (If any) (add extra sheet if required):

11. Experience relevant to the field of project (as per title of the project):

12. Why do you feel that you are the best candidate for this position?

13. Do you have interest in joining Ph.D. program? (Yes/No):

14. GATE details:

15. Additional information if any, which you would like to mention in support of your suitability for the post, (attach separate sheet if necessary)

16. Recommendation (Optional): *(Provide complete details like name, address, email ID and contact number)*

Declaration:

I have provided correct information as above and I understand that, if found incorrect, I may be disqualified for the position.

Date:

Place:

(Signature of the applicant)

Note: Candidate need to submit their application form along with all essential details and scanned copy of required documents through the following link within the last date. Candidate need to bring the complete hardcopy set of the application form with xerox copies of all required documents on the day of interview/test.

Link for submission of application form: <https://forms.gle/KhJcPseZ6Nkb3Vyb6>